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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/764,522

01/27/2004

Koji Aoki

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06/06/2005

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EXAMINER

HA, NGUYEN T

ART UNIT

PAPER NUMBER

2831

DATE MAILED: 06/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

EJ

Office Action Summary	Application No. 10/764,522	Applicant(s) AOKI, KOJI	
	Examiner Nguyen T. Ha	Art Unit 2831	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 January 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 7, 8, 11, 14 and 17 is/are rejected.
- 7) ☒ Claim(s) 2-6, 9, 10, 12, 13, 15, 16, 18 and 19 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>0104</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 7-8, 11, 14 and 17 are rejected under 35 U.S.C. 102(e) as being anticipated by Kiguchi et al. (US 6,887,631).

Regarding claim 1, Kiguchi et al. disclose a wiring board comprising:

- a substrate (302);
- a bank (412) which is disposed above the substrate to provide a plurality of regions (figure 9);
- a conductive layer (411) and first and second interconnecting lines (445 and 446) formed between the substrate and the bank; wherein:
- the bank has a top surface and a pair of side surfaces provided on both sides of the top surface (figure 9); and
- the side surfaces slopes symmetrically with respect to the substrate (figure 9).

Regarding claim 7, Kiguchi et al. disclose the conductive layer is one of electrodes of a capacitor (column 14, lines 12-15).

Regarding claim 8, Kiguchi et al. disclose the first and second interconnecting lines are respectively a signal line and a power supply line (column 14, lines 14-17).

Regarding claim 11, Kiguchi et al. disclose an electro-optical device comprising the wiring board and a functional layer disposed in each of the regions provided by the bank (figure 9).

Regarding claim 14, Kiguchi et al. disclose an electronic instrument comprising the electrode-optical device (figure 9).

Regarding claim 17, the method steps are necessitated by the device structure as it disclosed by Kiguchi et al. (figure 9) comprising:

- forming the wiring board (301)
- forming a functional layer (410) by disposing a liquid material including a functional layer material in each of the regions provided by the bank (412).

Allowable Subject Matter

3. Claims 2-6, 9-10, 12-13, 15-16 and 18-19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

With respect to claims 2-6, 10, 12-13, 15-16 and 18-19, the prior art alone or in combination does not teach the limitations of a wiring board comprising: the first interconnecting line is formed in a position closer to the substrate than the second interconnecting line, and the vertical centerline of the first interconnecting line is not coincide with the vertical centerline of the second interconnecting line, and the

conductive layer is formed in a position closer to the substrate than the second interconnecting line, and the vertical centerline of the conductive layer is not coincide with the vertical centerline of the second interconnecting line.

With respect to claim 9, the prior art alone or in combination does not teach the limitation of the first interconnecting line constitutes a part of a first device circuit, and the conductive layer and the second interconnecting line constitute a part of a second drive circuit.

Citation Relevant of Prior Art

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
 - a. Yamazaki et al. (US 6,787,807) disclose semiconductor device.
 - b. Hosomi (US 6,740,981) discloses semiconductor device including memory unit and semiconductor module.
 - c. Yamagata et al. (US 6,720,198) disclose light emitting device and manufacturing method thereof.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nguyen T. Ha whose telephone number is 571-272-1974. The examiner can normally be reached on Monday-Friday from 8:30AM to 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean Reichard can be reached on 571-272-2800 ext. 31. The fax phone

number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Nguyen T. Ha', with a stylized, cursive script.

Nguyen T. Ha
May 27, 2005